

SPECIFICATION For APPROVAL

()	Preliminary	Entry	Level	Specification
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(x) Final Specification

Title	10.4" VGA TFT LCD
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BUYER NAME	
MODEL NAME	

SUPPLIER	LG Philips LCD
MODEL NAME	LP104V2-W

SIGNATURE	DATE

Please return 1 copy for our confirmation

with your signature and comments.

APPROVED BY	DATE				
/G.Manager REVIEWED BY					
/S.Engineer PREPARED BY					
/ Engineer					
Product Engineering Dept.					
LG Philips LCD					

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Record of Revision

DATE AND VERSION	DESCRIPTION
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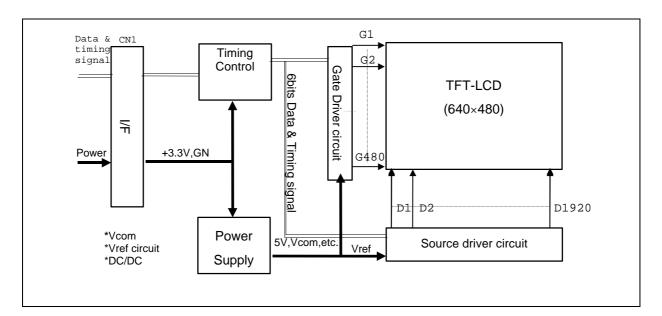
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1. G Description

The LG Philips LCD model LP104V2-W LCD is a Color Active Matrix Liquid Crystal Display with an integral Cold Cathode Fluorescent Tube(CCFT) back light system. The matrix employs a-Si Thin Film Transistor as the active element. It is a transmissive type display operating in the normally white mode. This TFT-LCD has a 10.4 inch diagonally measured active display area with VGA resolution(480 vertical by 680 horizontal pixel array). Each pixel is divided into Red, Green and Blue sub-pixels or dots which are arranged in vertical stripes. Gray scale or the brightness of the sub-pixel color is determined with a 6-bit gray scale signal for each dot, thus, presenting a palette of more than 262,144 colors.

The LP104V2-W LCD is intended to support applications where thin thickness, low power are critical factors and graphic displays are important. In combination with the vertical arrangement of the sub-pixels, the LP104V2-W characteristics provide an excellent flat panel display for office automation products such as Notebook PC.



General Display Characteristics

The following are general feature of the model LP104V2 LCD;

Active display area 10.4 inches(26.42cm) diagonal

Outsize dimensions $246.5(H) \times 179.4(V) \times 8.0(W) \text{ mm(typ)}$

Pixel pitch $0.33 \text{ mm} \times 0.33 \text{ mm}$

Pixel format 640 horiz. By 480 vert. Pixels RGB stripe arrangement

Color depth 6-bit, 262,144 colors

Display operating mode transmissive mode, normally white

Surface treatments hard coating(3H), anti-glare treatment of the front polarizer

Weight 415g (Typ.)

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2. Maximum Ratings

The following are maximum values which, if exceeded, may cause faulty operation or damage to the unit.

Table 1 ABSOLUTE MAXIMUM RATINGS

Parameter	symbol	Va	lues	Units	RMKS	
Farameter	Symbol	Min.	Max.	Offics		
Power Input Voltage Operating Temperature Storage Temperature	V _{DD} T _{OP} T _{ST}	3.0 0 -20	3.6 +50 +60	Vdc °	at 25° 1 1	

Note: 1. The Relative Humidity must not exceed 95% non-condensing at temperatures of 40° or less.

At temperatures greater than 40°, the wet bulb temperature must not exceed 39°.

2. Under no condition should the unit be exposed to corrosive chemicals.

3. Electrical Specifications

The LP104V2-W requires two power inputs. One is employed to power the LCD electronics and to drive the voltages to drive the TFT array and liquid crystal. The second input which powers the backlight CCFT, is typically generated by an inverter. The inverter is an external unit to the LCD.

Table 2 ELECTRICAL CHARACTERISTICS:

Parameter	Symbol	Values			Units	Notes
		Min.	Тур.	Max.		
MODULE: Power Supply Input Voltage Power Supply Input Current Power Consumption Ripple/Noise	V _{DD} I _{DD} P _{DD}	3.0	3.3 170 0.56	3.6 227 0.75	Vdc mA Watts	1
Logic Input Level, High Logic Input Level, Low	V _{IH} V _{IL}	2.0 Vss	-	VDD 0.8	V V	2 2
BACK LIGHT Lamp current Lamp voltage Lamp frequency Kick-Off Voltage	It V _t Ft Vk	2.0 490 40 -	5.5 505 60 -	6.0 630 80 845 1015	mA V _{RMS} KHz V _{RMS} V _{RMS}	3 25±2 0±2
Lamp life time Power Consumption	Lt PBL	20000 1.8	- 2.8	- 2.9	Hrs Watts	4

Notes: 1. The current draw and power consumption specified is for 3.3 Vdc at 25 , fv at 60Hz and black signal displayed.

- 2. Logic levels are specified for VDD of 3.3 Vdc at 25 . The values specified apply to all logic inputs; Hsync, Vsync, clock, data signals, etc.
- 3. Lamp frequency may produce interference with horizontal sync. frequency, and may cause beat on the display. Therefore lamp frequency shall be detached as much as from the horizontal sync. and from the harmonics of horizontal synchronous to avoid interference.
- DC/AC inverter for backlight is not built in this module.
 Back light power consumption shown above does not concern the efficiency of the inverter.

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4. Optical Specifications

Optical characteristics are determined after the unit has been 'ON' and stable for approximately 30 minutes in a dark environment at 25 $\,$. The values specified are at an approximate distance 50cm from the LCD surface at a viewing angle of Φ and θ equal to 0° .

Appendix A presents additional information concerning the specified characteristics.

Table 3 OPTICAL CHARACTERISTICS

Parameter	Symbol	Values		Units	RMKS	
		Min.	Тур.	Max.		
Contrast Ratio Surface Brightness Brightness Variation Response Time Rise Time Decay Time	CR Bsf B _V Tr Tr _R Tr _D	100 180 - -	150 200 - 30 50	- - 1.45	cd/m² msec	1 2 3 4
CIE Color Coordinates Red Green Blue White	X _R YR XG YG XB YB XW YW	0.513 0.304 0.287 0.470 0.126 0.121 0.292 0.309	0.543 0.334 0.317 0.500 0.156 0.151 0.322 0.339	0.573 0.364 0.347 0.530 0.186 0.181 0.352 0.369		±0.03
Viewing Angle x axis, right (=0°) x axis, left(=180°) y axis, up(=90°) y axis, down (=270°)	x x y y	70 70 45 50			degree, °	5
Flicker	,					6

Notes:

- Contrast Ratio (CR) is defined mathematically as: (Surface Brightness with all white pixels) ÷ (Surface Brightness with all black pixels)
- 2. Surface brightness is measured on the LCD surface 50cm from the surface with all pixels displaying white. For more information see Appendix A 1.
- 3. The variation in surface brightness, SB_V is determined by measuring B_{ON} at each test position 0 through 9, and then dividing the maximum B_{ON} of 9 points luminance by minimum B_{ON} of 9 points luminance. For more information see Appendix A 1. SB_V Maximum (B_{ONO} , B_{ON1} , B_{ON9}) ÷ Minimum (B_{ONO} , B_{ON1} , B_{ON9})
- Response time is the time required for the display to transition from white to black (Rise Time, Tr_R) and from black to white (Decay Time, Tr_D). For additional information see Appendix A - 2.
- 5. Viewing angle is the angle at which the contrast ratio is greater than 5. The angles are determined for the horizontal or x axis and the vertical or y axis with respect to the z axis which is normal to the LCD surface. For more information see Appendix A 3
- 6. When the lcd is refreshed at 60Hz rate with the backlight on, there are no noticeable flicker at line by line pattern (gray level 0/31) when the screen is viewed with naked eye.

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Table 4 Luminance of Grey Level

	Luminance	Luminance
Gray Level	(%)(min)	(%)(max)
LO	0.0	1.0
L7	0.0	3.0
L15	0.0	6.0
L23	1.5	15.0
L31	5.0	38.0
L39	20.0	70.0
L47	50.0	90.0
L55	80.0	98
L63	100	100

5. Environment

5.1 Altitude :

operating 0 - 10,000 feet (3048m) storage/shipment 0 - 40,000 feet (12192m)

5.2 Corrosive gas: use at indoor & outdoor in a big city.

5.3 Environment test condition

No.	Test ITEM	Conditions				
1	High temperature storage test	Ta = 60 240h				
2	Low temperature storage test	Ta = -20 240h				
3	High temperature & high humidity operation test	Ta = 40 95%RH 240h (no condensation)				
4	High temperature operation test	Ta = 50 240h				
5	Low temperature operation test	Ta = 0 240h				
6	Vibration test (non-operating)	- Sine wave, 10 to 500Hz, 1.5G, 0.5oct/min, 3axis, - Random Truck : 6-12-18-42-80-200Hz 0.5Grms, 30min. 3axis - Random Air : 6-18-200-500Hz 2.1Grms, 30min. 3axis				
7	Shock test (non-operating)	- half sine wave, 180G, 2ms, one shock of each six faces (i.e. run 180G 2ms for all six faces.)				

{Result Evaluation Criteria}

There should be no change which might affect the practical display function when the display quality test is conducted under normal operating condition.

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6. Interface Pin Configuration

Used connector: DF9B-31P-1V(HIROSE), Matching side: DF9B-31S-1V(HIROSE)

Table 5 INTERFACE PIN CONFIGURATION

Pin	Symbol	Description	Notes
1 2 3 4	GND CLK Hsync Vsync	Ground Data clock Horizontal sync. Vertical sync.	I/F PIN ARRANGEMENT (Transparent view) 3
5 6 7 8 9 10 11 12 13 14 15 16 17 18	GND R0 R1 R2 R3 R4 R5 GND G0 G1 G2 G3 G4 G5 GND	Ground Red data(LSB) Red data Red data Red data Red data Red data Red data Red data(MSB) Ground Green data(LSB) Green data	2
20 21 22 23 24 25 26 27 28 29 30 31	B0 B1 B2 B3 B4 B5 GND DTMG VDD VDD VDD NC NC	Blue data(LSB) Blue data Blue data Blue data Blue data Blue data Blue data Blue data(MSB) Ground Data timing signal Power supply +3.3V Power supply +3.3V No connection No connection	* NC (30, 31pin) should be electrically opened during operation. * The metal top case is connected to GND. * All GND(ground) pins should be connected together and to Vss which also be connected to the LCD's metal frame. * All Vdd(power input) pins should be connected together.

The backlight interface connector is a model BHR-03VS-1, manufactured by JST. The mating connector part number is SM02(0.8)B-BHS-1-TB or equivalent. The pin configuration for the connector is shown in the table below.

Table 6 BACKLIGHT CONNECTOR PIN CONFIGURATION

Pin	Symbol	Description	Notes				
1	HV	Lamp power input	1				
2	NC	NC					
3	LV	Ground	2				

Notes: 1. The input power terminal is colored pink. Ground pin color is black. 2. The backlight ground should be common with Vss.

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7. Signal Timing Specification

This is the signal timing required at the input of the ASIC inside module. All of the interface signal timing should be satisfied with the following specifications for it's proper operation.

Table 7 Timing Table

ITEM		Symbol	MIN.	TYP.	MAX.	UNIT	NOTE		
	Frequency	fCLK	21	25	32	MHz			
	Width-Low	t _{WCL}	7	-	-				
DCLK	Width-High	t _{WCH}	7	-	-	ns			
	Rise Time	t _{rCLK}	-	-	20				
	Fall Time	t _{fCLK}	-	-	20				
	Duty	D	0.45	0.5	0.55	-	D=t _{CLKL} /t _{CLK}		
	Set up Time	t _{SH}	0	-	-		ns		
	Hold Time	t _{HH}	15	-	-		110		
Hsync	Period	t _{HP}	770	800	900		t _{clk}		
	Width-Active	t _{WH}	9	-	128		CCLK		
	Rise/Fall Time	t _{Hr} , t _{Hf}	-	-	30	ns			
	Set up Time	t _{SV}	0	-	-	t _{CLK}	for Hsync		
	Hold Time	t _{HV}	15	-	-		TOT HISTING		
Vsync	Period	t _{VP}	515	525	560	t _{HP}			
	Width-Active	t _{WV}	1	-	15				
	Rise/Fall Time	t_{Vr}, t_{Vf}	-	-	50	ns			
	Set up Time	t _{SI}	0	-	-	ns	for DCLK		
	Hold Time	t _{HI}	15	-	-		TOT DCLK		
DTMG	Rise/Fall Time	t _{lr} , t _{lf}	-	-	30	ns			
	Horizontal	t _{HBP}	10	-	-				
	Back Porch					t _{CLK}			
	Horizontal	t _{HFP}	10	-	-				
	Front Porch								
	Vertical	t_{VBP}	34	-	-				
	Back Porch					t _{HP}			
DTMG	Vertical	t _{VFP}	1	-	-				
	Front Porch								
	Set up Time	t _{SD}	0	-	-				
DATA	Hold Time	t _{HD}	15	-	-	ns	for DCLK		
	Rise/Fall Time	t_{Dr}, t_{Df}	-	-	20				

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8. Signal Timing Wave form

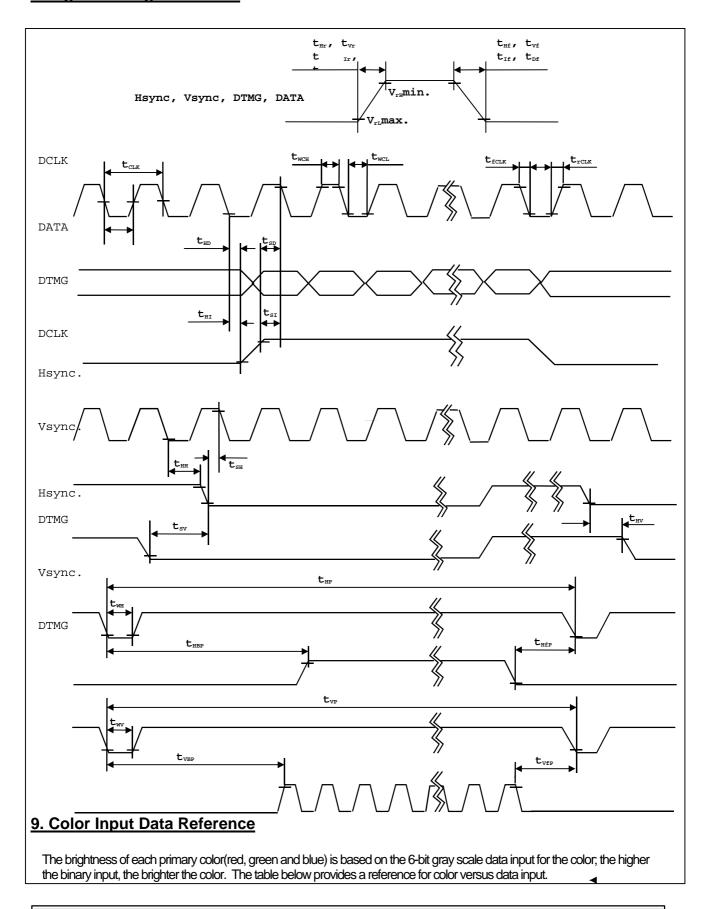




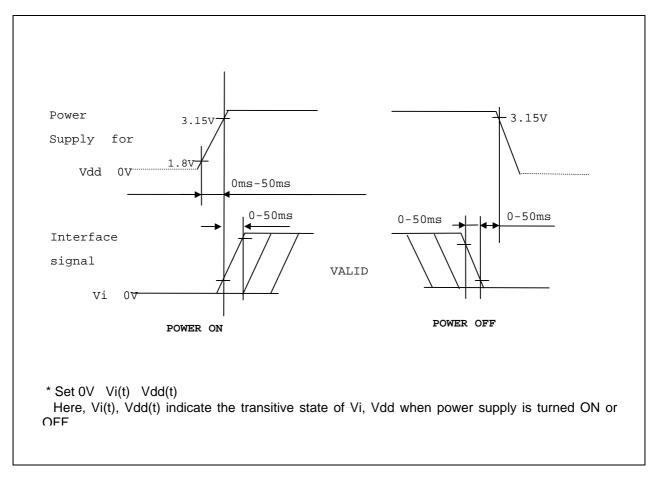
Table 8 COLOR DATA REFERENCE

	Input Color Data																		
Color		Red					Green					Blue							
		MSB LSB			MSB LSB				MSB LSB					\$					
		R5	R4	R3	R2	R1	R0	G5	G4	G3	G2	G1	G0	B5	B4	В3	B2	B1	B0
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(63)	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	Green(63)	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
Basi	Blue(63)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1
С	Cyan	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1
Color	Magenta	1	1	1	1	1	1	0	0	0	0	0	0	1	1	1	1	1	1
S	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	D1/00\ D1-			•	_		_		_	•	•	_		•	_		•	_	
	Red(00) Dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(01)	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0
Red	Red(02)				_	_	0	0		_				_	0	_	0		0
Red	Red(61)	1	1	1	1	: 0	1	0	0	:	0	0	0	: 0	: 0	: 0	: 0	0	0
	Red(61)		1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(63) Bright	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	Green(00)Dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Green(01)	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0
	Green(02)	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0
Gree	010011(02)		Ĭ		:	:	:	:		:		:	.	:	:	:	:		:
n	Green(61)	0	0	0	0	0	0	1	1	1	1	0	1	0	0	0	0	0	0
	Green(62)	0	Ô	Ö	Ö	Ö	Ö	1	1	1	1	1	Ö	Ö	Ö	Ö	Ö	0	Ö
	Green(63)Bright	Ö	ō	0	Ö	Ō	0	1	1	1	1	1	1	0	Ö	ō	0	Ō	0
									-	-	-	-							
	Blue(00) Dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Blue(01)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
	Blue(02)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0
Blue		:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	Blue(61)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	0	1
	Blue(62)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	0
	Blue(63) Bright	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1

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10. Power On/Off Sequence



Notes: 1. Please avoid floating state of interface signal at invalid period.

2. When the interface signal is invalid, be sure to pull down the power supply for LCD V_{DD} to 0V.

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11. Mechanical Characteristics

The chart below provides general mechanical characteristics for the model LP104V2-W LCD. In addition, the figure below is a detailed mechanical drawing of the LCD. Note that dimension are given for reference purposes only.

Outside dimensions:

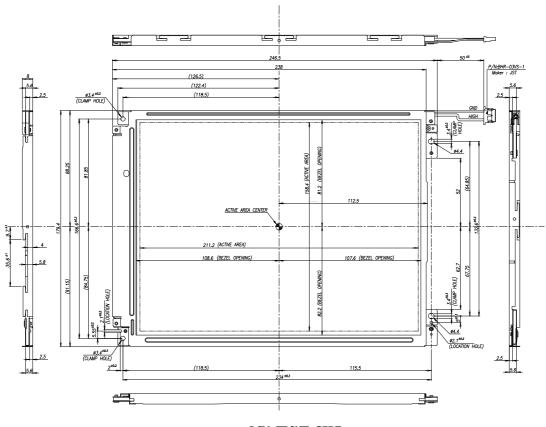
Width 246.5mm(TYP.)

Height 179.4mm(TYP.)
Thickness 8.0mm(TYP.)

Active Display area:

Width 211.2mm Height 158.4mm

Weight (approximate): $415(\pm 10)$ g



< LCM FRONT SIDE >

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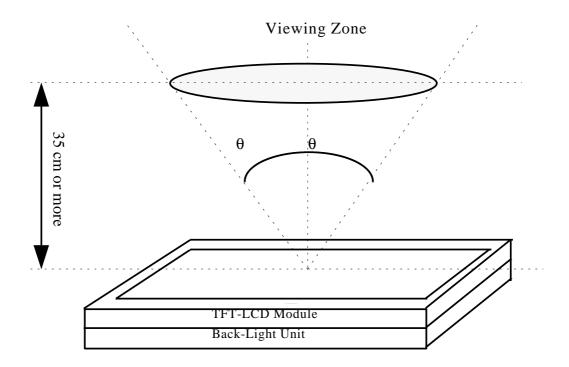


12. COSMETICS

This cosmetic inspection is related to the acceptance quality levels of any defects in the LCD module which we produce. For more information about the following, contact LG Philips LCD any time.

12.1 Cosmetic Inspection Conditions

- 12.1.1 Inspective viewing angle
 - This inspection should be executed according to the following figure.



- Viewing angle should be in the range of :

q < 45 0 when non-operating inspection

q < 20⁰ when operating inspection (Horizontal direction)

q < 10⁰ when operating inspection (Vertical direction)

12.1.2 Environment Conditions

- Ambient temperature : 25 ± 5 ^OC - Ambient Humidity: 65 ±5 % RH

- Ambient Lumination: Using single 20 watts fluorescent lamp (about 500 lux)

- Distance by eyes of inspector from the module : 35 cm or more

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13. Safety

- 13.1 Sharp edge: There will be no sharp edges or corners on the display assembly that could cause injury.
- 13.2 materials
 - 13.2.1 Toxicity: There is no carcinogenic materials used anywhere in the display module.
 - 13.2.2 Flammability: All components including electrical components of the module should meet the flammability grade Min.UL94-V1. The printed circuit board is made from material rated UL94-V1 or better. The actual UL flammability rating is printed on the printed circuit board.
 - 13.2.3 Capacitors: If any polarized capacitors are used in the display assembly, provisions will be made to keep them from being inserted backwards.
 - 13.2.4 Hazardous Voltages: Any points exceeding 42.2volts meets the requirement of the limited current circuit. The current through a 2 Kohm resistance is less than 0.7 x f(Khz)mA.

14. Packaging

: The packaging of the LCD meets 75 cm drop test.

15. Smoke free design

: No smoke or strange smell shall not be observed by the operator as a result of any single failure. Display technology will demonstrate it to the representative by open/short test in design verification test for approval if there is any default symptoms.

16.Performance

: All electromagnetic compatibility apply when the display module is attached to the host system.

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17.PRECAUTIONS

Please pay attention to the followings when you use this TFT/LCD module with Back-light unit.

17.1 MOUNTING PRECAUTIONS

- (1) You must mount a module using holes arranged in four corners.
- (2) You should consider the mounting structure so that uneven force (ex. twisted stress) is not applied to the module.
 - And the case on which a module is mounted should have sufficient strength so that external force is not transmitted directly to the module.
- (3) Please attach the surface with a transparent protective plate in order to protect the polarizer LC cell.
 - Transparent protective plate should have sufficient strength in order to resist external force.
- (4) You should adopt radiation structure to satisfy the temperature specification.
- (5) Acetic acid type and chlorine type materials for the cover case are not desirable because the former generates corrosive gas of attacking the polalizer at high temperature and the latter causes circuit break by electro-chemical reaction.
- (6) Do not touch, push or rub the exposed polarizers with glass, tweezers or anything harder than HB pencil head.
 - And Please do not rub with dust clothes with chemical treatment.
 - Do not touch the surface df polarizer for bare hand or greasy cloth. (Some cosmetics are detrimental to the polarizer.)
- (7) When the surface becomes dusty, please wipe gently with absorbent cotton or other soft materials like chamois soaked with petrolium benzene. Normal-hexane is recommended for cleaning the adhesives used to attach front / rear polarizers. Do not use acetone, toluen and alcohol because they cause chemical damage to the polarizer.
- (8) Wipe off saliva or water drops as soon as possible. Their long time contact with polarizer causes deformations and color fading.
- (9) Do not open the case because inside circuits do not have sufficient strength.

17.2 OPERATING PRECAUTIONS

- (1) The spike noise causes the mis-operation of circuits. It should be lower than following voltage:
 - VDD = \pm 200mV, V1 = \pm 200mV (Over and under shoot voltage).
- (2) Response time depends on the temperature. (In lower temperature, it becomes longer.)
- (3) Brightness depends on the temperature. (In lower temperature, it becomes lower.)
 And in lower temperature, response time (required time that brightness is stable after turned on) becomes longer.
- (4) Be careful for condensation at sudden temperature change. Condensation makes damage to polarizer or electrical contacted parts. And after fading condensation, smear or spot will occur.
- (5) When fixed patterns are displayed for a long time, remnant image is likely to occur.
- (6) A module has high frequency circuit. If you need to shield the electromagnetic noise, please do in yours.
- (7) When a Back-light unit is operating, it sounds. If you need to shield the noise, please do in yours.

17.3 ELECTROSTATIC DISCHARGE CONTROL

Since a module is composed of electronic circuits. it is not strong to electrostatic discharge. Make certain that treatment persons are connected to ground through wrist band etc.. And don't touch I/F pin directly.

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17.4 PRECAUTION FOR STRONG LIGHT EXPOSURE

Strong light exposure causes degradation of polarizer and color filter.

17.5 STORAGE

When storing modules as spares for a long time. The following precautions are necessary.

- (1) Do not leave the module in high temperature, especially in high humidity for a long time.
- (2) Store the module not to expose to the sunlight directly.
- (3) Avoid condensation of water. It may cause mis-operation.

17.6 HANDLING PRECAUTIONS FOR PROTECTION FILM

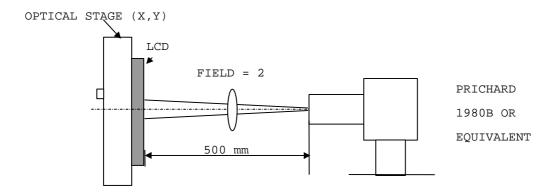
- (1) Be sure to turn off the power when connecting or disconnecting circuit.
- (2) Note that polarizer are easily damaged, pay attention not to scratch or press these surface with any hard object.
- (3) When Icd surface become dirty, please wipe them off with soft material like absorbent cotton.
- (4) Protect the module from the ESD. AS it may damage electronic circuit (C-MOS) Make certain that treatment persons body are grounded through wrist bend.
- (5) Do not disassemble the module and be careful not to have strong mechanical shock such as twist when install to the cabinet. It may cause damage permanently.

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A-1 Measurement setup & method

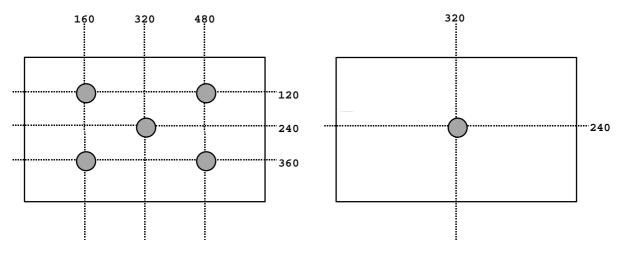
Optical measurement shall be executed in a dark room or equivalent state with the following method. Measurement shall be executed 30 minutes after back light turn on.



A-2 Brightness

<measuring point for brightness vibration>

<measuring point for surface brightness >

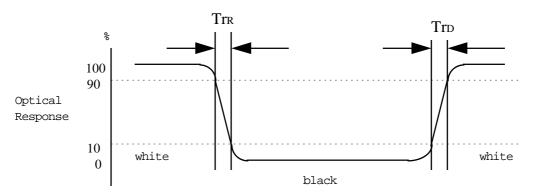


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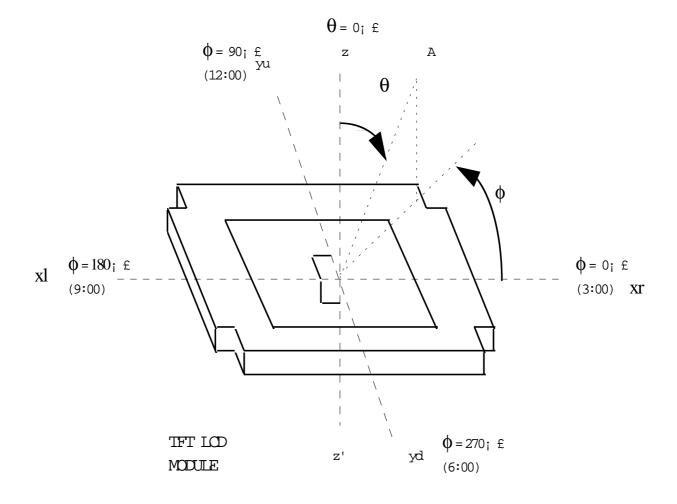
A-3 RESPONSE TIME

The response time is defined as the following figure and shall be measured by switching the input signal for "black" and "white".



A-4 Viewing angle

<Definition of viewing angle range>



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